



## Mrs. Poonam Swapnil Jadhav

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(Pulachi) Tal - Hatkanangale, Dist - Kolhapur- 416 122.

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**Date of Birth :** 16<sup>th</sup> November 1986

**Marital Status :** Married

**Religion:** Hindu

**Cast:** Maratha

### Education

#### Ph.D. in Electronics

Shivaji University, Kolhapur

Date of Registration

01-07-2015

Pursuing

(Thesis Submitted)

#### M. Sc. (Electronics)

Shivaji University, Kolhapur

Distinction (80.54)

**2009**

#### B. SC. (Electronics)

Shivaji University, Kolhapur

Distinction (80.36)

**2007**

#### H.S.C.

Divisional Board, MSBSHSE, Kolhapur

Distinction (79.33)

**2004**

#### S.S.C.

Divisional Board, MSBSE, Kolhapur

Distinction (81.33)

**2002**

### Ph. D. Project Details

**Project Title: FPGA Based Fuzzy Logic Spindle Temperature Compensation** (to design and implement the Real Time Fuzzy Logically Controlled Cooling System for Thermal Compensation of High Speed Precision Machine).

**Project Guide:** Dr. R. R. Mudholkar

### M. Sc. Project Details

**Project Title: Weather Monitoring System** (to measure a weather parameters temperature and humidity and make data storage system by using ATMEGA 32 microcontroller, which are required at the most of the times in industries, weather stations to know about the weather.)

**Project Guide:** Dr. S. R. Sawant

### B. Sc. Project Details

**Project Title: School College Quiz Buzzer** (to detect first respondent. Detects the first respondent immediately after pressing the buzzer).

**Project Guide:** Dr. V. C. Mahajan

### Additional Qualifications

- Qualified **National Eligibility Test** (NET) for Lectureship in the subject Electronic Science in December 2012, conducted by UGC, New Delhi.
- Qualified **State Eligibility Test** (SET) for Lectureship in the subject Electronic Science in December 2013, conducted by Savitribai Phule Pune University as State Agency.
- Completed "**Certificate Course in C Programming**" with A grade in February-March 2007 in Login Computer Academy, Tarabai Park, Kolhapur.
- Passes **MSCIT examination with 90 percent** in October 2006, conducted by MSBTE and MKCL, Mumbai.

## Awards & Rewards

- Worked as **Junior Research Fellow** for two years on **UGC-MRP** entitled as “Studies on Fuzzy Logic Thermal Compensation Technique for High Speed Precision Machine” at Department of Electronics, Shivaji University, Kolhapur.
- **Late Shantaram Narayan Waingankar Paritoshik** for securing highest marks in electronics 2008-2009, conferred by Shivaji University, Kolhapur.
- **Secured Seventh Rank in** B. Sc. III Electronics Examination of April 2007, conducted by Shivaji University, Kolhapur.
- Secured **Second Prize in** One Day UGC-CPE Sponsored **National Conference** on “Innovative Trends in Electronics and Allied Technology (ITEAT 2017)” organized by Department of Electronics and Computer Science, Yashwantrao Chavan Institute of Science, Satara in February 2017.

## Academic Experience

**Academic Experience:** 7 years (Subject: Electronics)

At D. Y. Patil Junior College, Kadamwadi, Kolhapur

**Teaching Experience:** 7 years (Subject: Electronics)

At D. Y. Patil Junior College, Kadamwadi, Kolhapur.

## Research Experience

**Research Area:** Embedded Systems, Fuzzy Logic and VLSI

**Research Experience :** 5 Years

1. Two Year in M. Sc. ( Electronics) at Department of Electronics, Shivaji University, Kolhapur
2. Four Years in Ph. D. ( Electronics ) at Department of Electronics, Shivaji University, Kolhapur

## Publications

1. Poonam S. Patil, S. R. Sawant, R.R. Mudholkar, “AVR Micro-Controller Based Embedded Weather Monitoring System , International Journal for Research in Applied Science & Engineering Technology (IJRASET). Volume 4 Issue VII, July 2016.PP:245-250.
2. Poonam S. Patil, R. R. Mudholkar, “Cooling Techniques for a spindle of Machine Tool”, International Journal of Engineering and Computer Science, Vol:5(12), 2016,pp-19653-19656.
3. Poonam S. Patil, R. R. Mudholkar, “Thermal Error Modeling in a Machine Tool” International Journal of Engineering Science and Technology. Vol:9(5), 2017, pp:344-352
4. Poonam S. Patil, R. K. Kamat, R. R. Mudholkar, “TF Estimation and Thermal Behavioral Analysis of spindle cooler System Prototype” Research Journal of Engineering and Technology, Vol:9(1),2018, pp:37-44.
1. Poonam S. Patil, R. R. Mudholkar, “Interdisciplinary Relevance of Fuzzy Logic: A Review”, National Conference on "Innovative Trends in Electronics and Allied Technology" (ITEAT-2017), Organized by Rayat Shikshan Sanstha"s Yashwantrao Chavan Institute of Satara, Department of Electronics and Computer science, Sponsored by UGC-CPE New Delhi, Technically Sponsored by Society for Promotion of Excellence in Electronic Discipline (SPEED), Pune. (Adjudged for **second prize** for the best poster presentation.)

## Conferences and Workshops

**SELF-  
ASSESSMENT**

2. Participated two day national workshop on Big Data Analytics with Hadoop organized by Department of Computer Science, Shivaji University, Kolhapur on 5<sup>Th</sup> and 6<sup>Th</sup> April 2017.
3. Poonam S. Patil, R.R. Mudholkar “Student Performance Evaluation By Different Defuzzification Techniques” International Interdisciplinary Conference On Curriculum Reformsin Higher Education: Global Scenario (IICCRHE-2018) February 4-6, 2018

Self-motivated and loyal individual with a strong passion for work and teaching profession. Being a responsible person with a good analyzing mind helps me to combat every difficulty and face challenging assignments successfully and with ease.

I hereby solemnly, do affirm that the particulars furnished above are true to the best of my knowledge.

**Date:**

26<sup>Th</sup> November 2019

**Place:**

Kolhapur

Mrs. Poonam Swapnil Jadhav